

FIG. 1

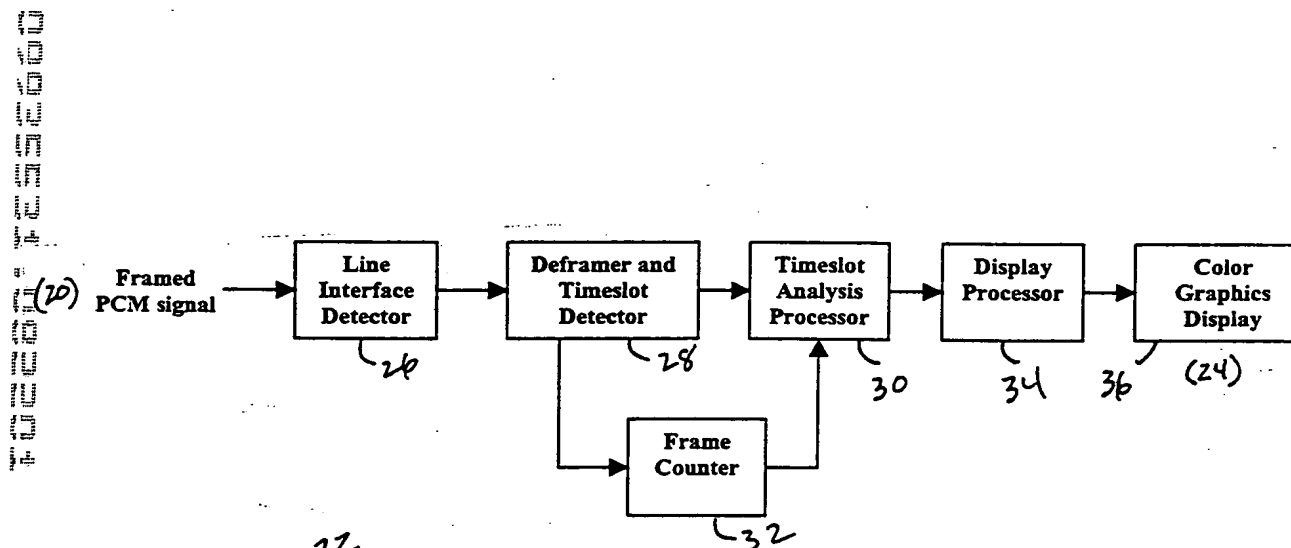


FIG. 2

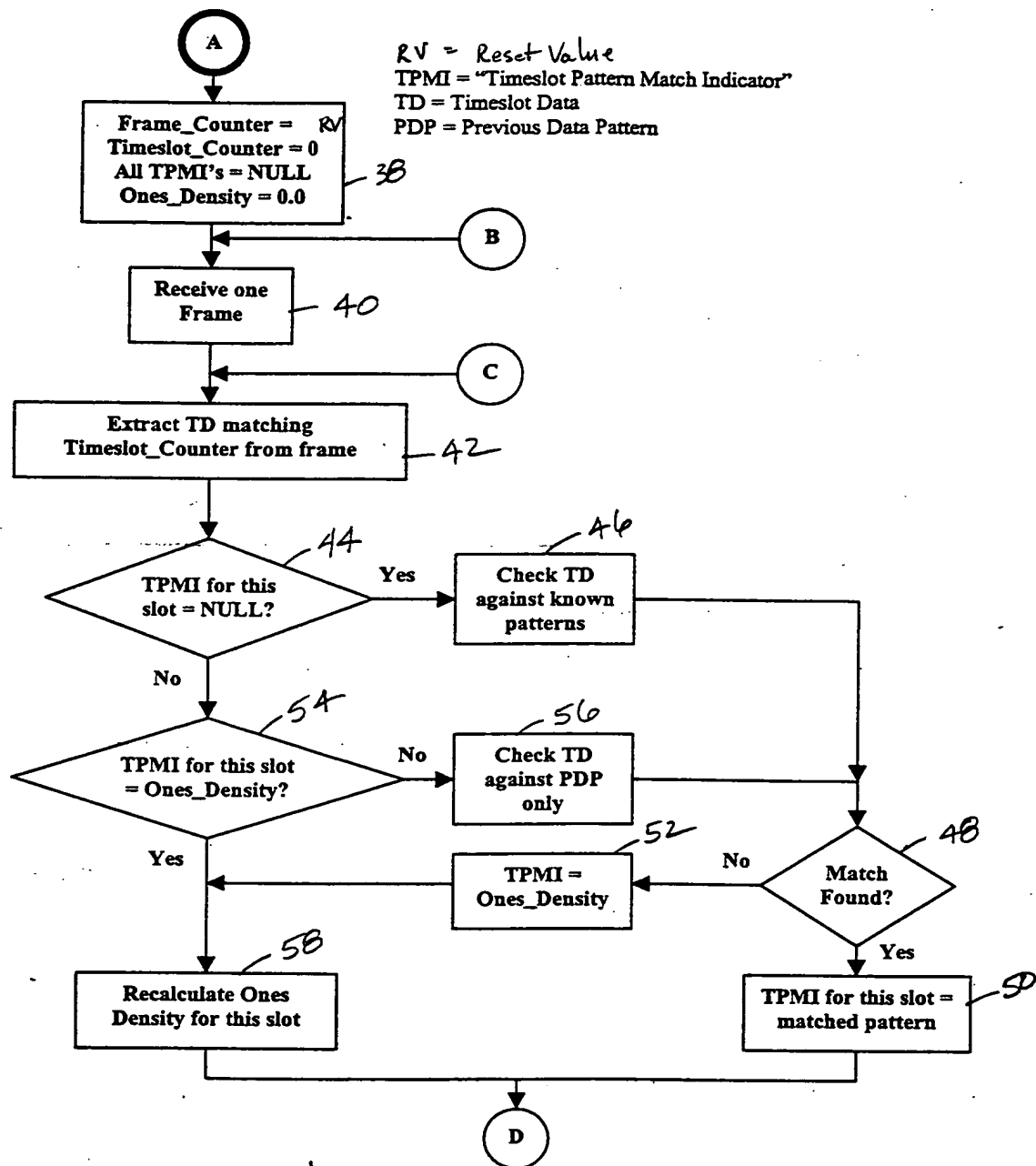


FIG. 3A

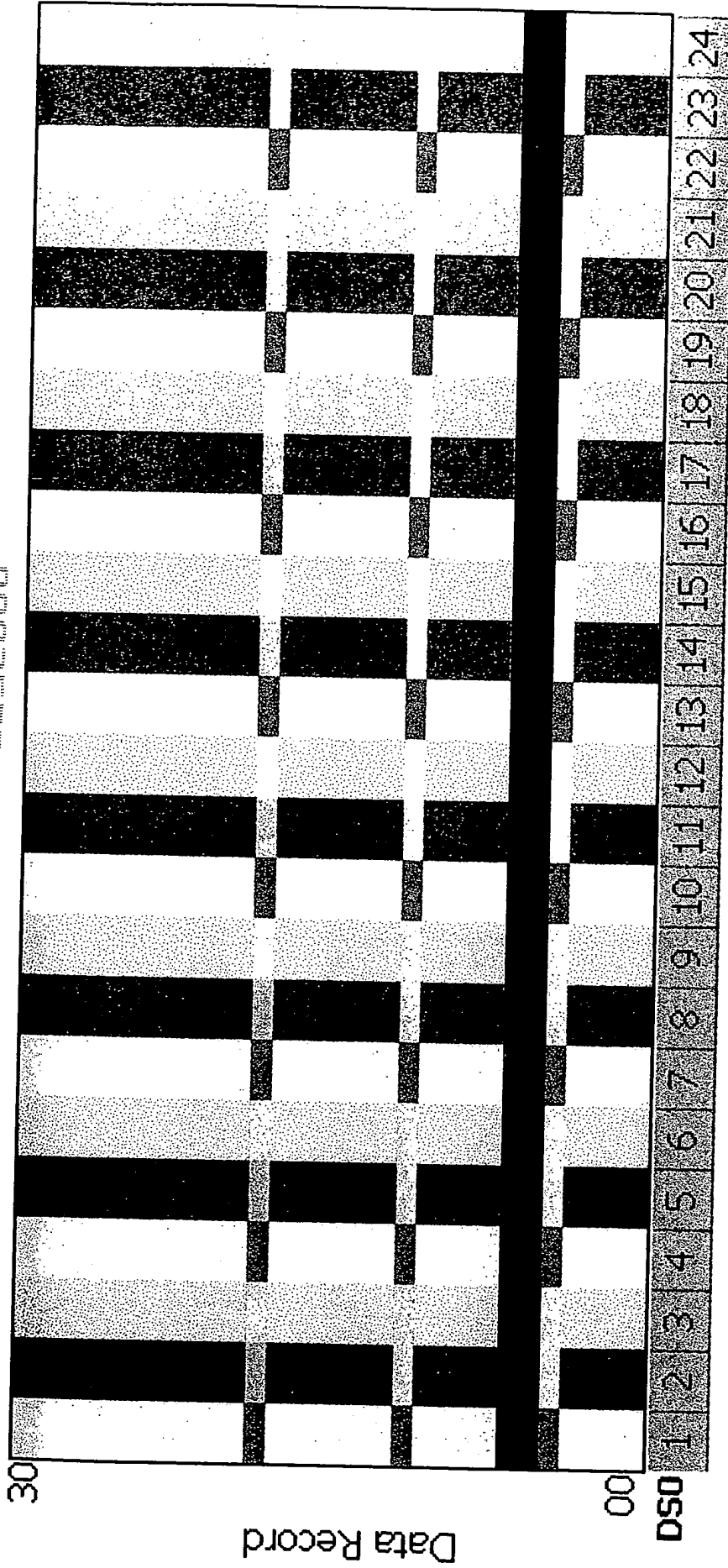
```

graph TD
    D((D)) --> 60{Last Time Slot?}
    60 -- Yes --> 62[Timeslot_Counter = 0]
    60 -- No --> 64[Increment Timeslot_Counter]
    62 --> 66{Last Frame?}
    66 -- Yes --> 70[Output one line of display]
    66 -- No --> 68[Increment Frame_Counter]
    70 --> 72[Frame_Counter = RV]
    64 --> C((C))
    68 --> B((B))
    72 --> A((A))

```

F16.3B

DSO Activity Graph



% Ones Density:

Updates

90 70 50 30 10

Patterns

All

Idle

Ones

Pattern

Zeros

All
Ones
Pattern
Zeros

Close

Fl6.4

Test Start: 8/15/1 14:55:47

Technician:

Notes